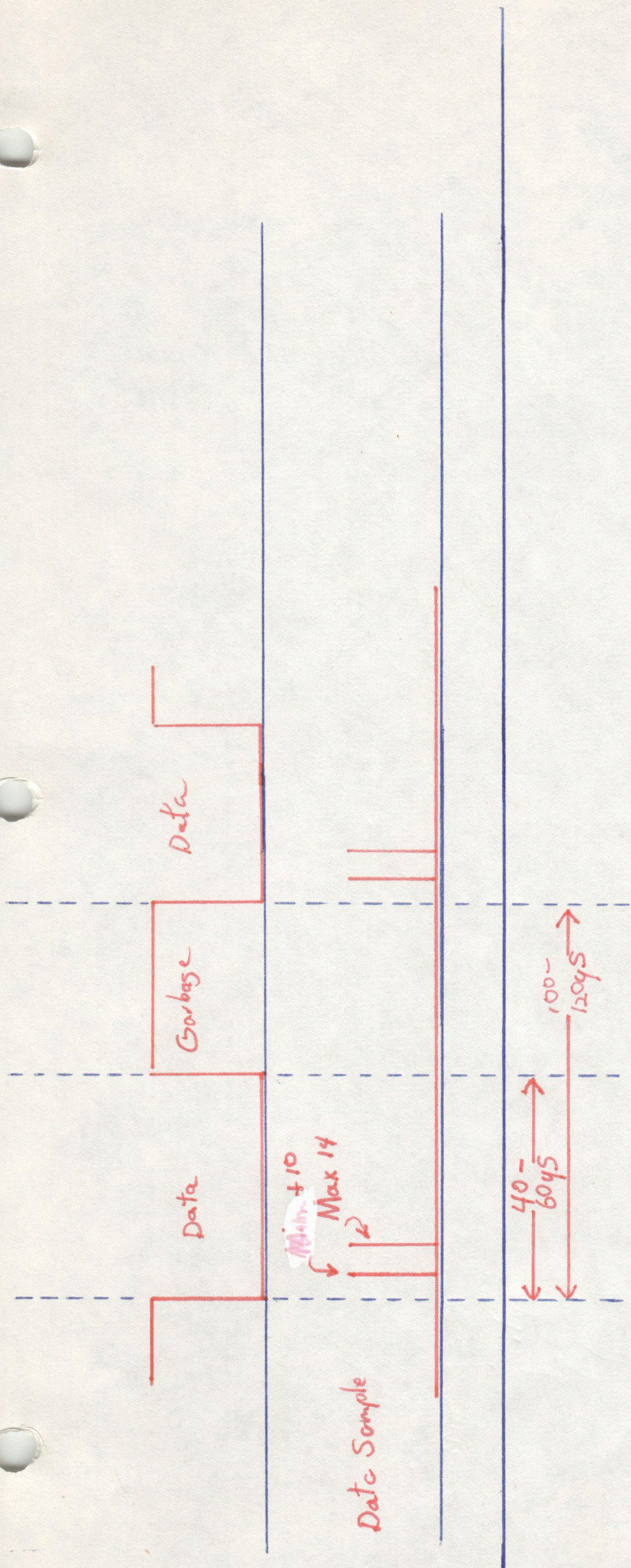


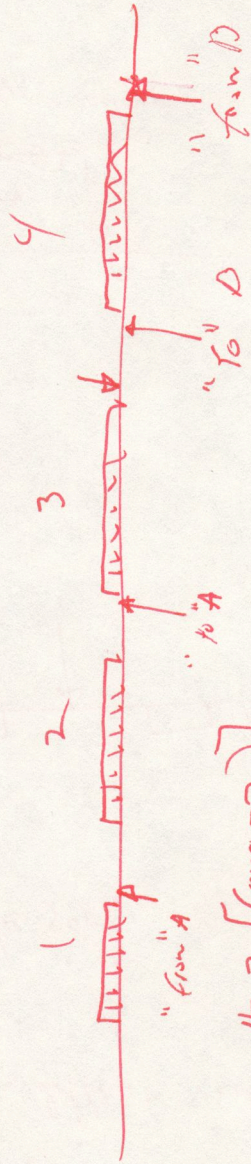
Interface Signal Assignment 2-8-79 T.Jennings



Data Samples

Data Sampling Timing
 T. Jennings 2-11-74

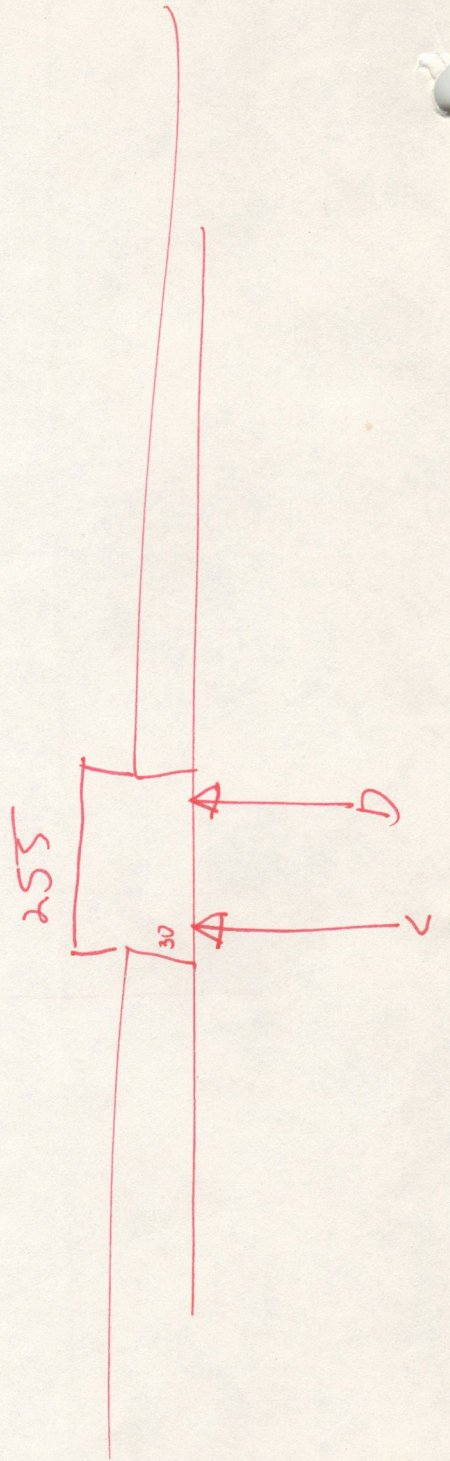
filegap time = filegap - 2 (red gaps)



A. open # 3 [open=2]

start fully = 200 '10

skip 1 filegap



7/31/78

MPL # 10-16-78

Write Flags

	ISFC	Sync End
	ISRL	" Rev
	IRWC	Rewind
	ISWS	Set write status
	IOVW	Overwrite
	IWDS	Date Strobe
	IWARS	W Amp Reset
	IRTH	Read Thresh

Or	1	BN	40	6
Yw	2	rd	41	6
Bl	3	OR	42	6
vi	4	YV	43	6
Bw	8	GY	44	6
Bn	6	BL	45	6
rd	7	VI	45	6
or	AC	-	-	-

~~to~~ TRQAS

Read flags

	IRDY	Ready
	ILDp	Bot.
	IFPT	Protect
	BTIM	Bit timer, 960MHz X16 600 Baud

WT	8		A0	5
BK	10		A2	5
BN	11		A1	5
	-		A7	5
			CA1	5

KAZ

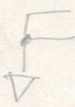
Write Data

	IWDP	Parity
	IWD2	2 ⁵
	IWD3	
	IWD4	
	IWD5	
	IWD6	
	IWD7	2 ⁰

rd	12		B6	6
OR	13		B5	6
Yw	14		B7	6
Bn	15		B3	6
B3L	16		B2	6
vi	17		B1	6
GY	18		B0	6

strobe (04)
strobe

Read Data



	IRDP	Parity
	IRD2	
	IRD3	
	IRD4	
	IRD5	
	IRD6	
	IRD7	

GN	5			
WT	19		B6	5
BK	20		B5	5
BN	21		B4	5
rd	22		B3	5
OR	23		B2	5
Yw	24		B1	5
GN	25		B0	5

OK 8/8/74
10-16-78